



**SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
PTO-1449 FORM**

ATTY. DOCKET NO.
10746/9

U.S. SERIAL NO.
09/376,904

APPLICANT(S)
Seisho YASUKAWA, et al.

FILING DATE
August 18, 1999

GROUP
2331 2562

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
D.O.	5,337,308	August 9, 1994	Ruixue Fan	—	—	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

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OTHER DOCUMENTS

EXAMINER INITIAL		Technology Center 2600
		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

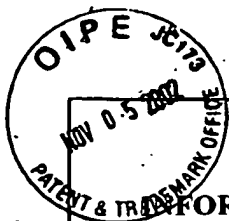
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[Signature]

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12/5/04

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PTO-1149 FORM

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GROUP
273T 2662

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE*
D.O.	5,355,372	October 11, 1994				

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FOREIGN PATENT DOCUMENTS

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						YES	NO

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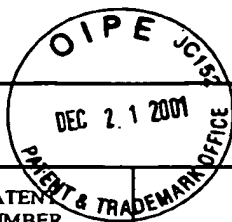
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	APPLICANTS YASUKAWA et al.	
	FILING DATE August 18, 1999	GROUP 2731 2662



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U. S. PATENT DOCUMENTS

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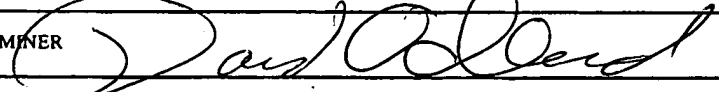
FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
D.O.	6-77984	March 18, 1994	Japan	—	—	abstract	
	5-207062	August 13, 1993	Japan	—	—	abstract	
	6-252948	September 9, 1994	Japan	—	—	abstract	
	2-195758	August 2, 1990	Japan	—	—	abstract	
D.O.	4-252632	September 8, 1992	Japan	—	—	abstract	

OTHER DOCUMENTS

EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
D.O.	Oki, et al, "A Scalable Distributed Arbitration in High-Speed ATM Switches", IEICE General Conference, B-6-41, 1997.**
D.O.	Yasukawa, et al., "Multi-Route ATM Switch Which Conserve Cell Sequence in Tournament Manner", IEICE General Conference, B-6-19 1998.**

** With English Translation.

EXAMINER 	DATE CONSIDERED 12/5/01
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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LIST OF REFERENCES CITED BY APPLICANT

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Seisho Yasukawa, et al.

FILING DATE

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1584 U.S. Pat.
09/376904
08/18/99

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

D.O.	AR	Tom Chaney, J. Andrew Fingerhut, Margaret Flucke, Jonathan S. Turner,
		"DESIGN OF A GIGABIT ATM SWITCH," IEEE INFOCOM'97, 1997.
1	AS	Jonathan S. Turner, N.Yamanaka, "Architectural Choices in Large Scale ATM
		Switches," IEICE TRANS. COMMUN., VOL. E81-B. NO.2 FEBRUARY 1998.
D.O.	AT	M. Collivignarelli, A.Daniele, G. Gallassi, F. Rossi, G. Valsecchi, L. Verri,
		"System and Performance Design of the ATM Node UT-XC," SUPERCOMM/ICC'94.

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D.O.	AR	G. Balboni, M. Collivignarelli, L. Licciardi, A. Paglialunga, G. Rigolio, F. Zizza, "From transport backbone to service platform: facing the broadband switch evolution;" ISS'95.
	AS	
D.O.	AS	M.A. Henrion, G.J. Eilenberger, G.H. Petit, P. Parmentier, "TECHNOLOGY, DISTRIBUTED CONTROL AND PERFORMANCE OF A MULTIPATH SELF-ROUTING SWITCH," ISS'92.

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